

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.:

Taylor R. Efland

Art Unit:

2826

Filed:

09/975,630 10/12/01

Examiner:

Leonardo Andujar

For:

Docket:

CIRCUIT STRUCTURE INTEGRATING THE POWER DISTRIBUTION FUNCTIONS OF CIRCUITS AND LEADFRAMES INTO THE CHIE

ELECTION

July 18, 2002

Commissioner of Patents and Trademarks Washington, D.C. 20231

CERTIFICATE OF MAILING UNDER 37 C F.R. 18 (A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to. Commissioner for Patents and Trademarks, Washington, D.C. 20231 on July 18, 2002.

Sir:

Responsive to the Office Action dated June 28, 2002, please enter the following:

For prosecution, Applicant elects Group I, Claims 1-19, drawn to a semiconductor device, classified in class 257, subclass 691.

Applicant acknowledges that Claims 20-23 are withdrawn from further examination.

A prompt action on the merits is respectfully requested.

spectfully submitted,

Reg. No. 20,25

Attorneys for Applicant

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COPY OF PAPERS ORIGINALLY FILED

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July 18, 2002

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Renaissance Tower 1201 Elm Street, Suite 1700 Dallas, Texas 75270 214 939 4400 214 760 7332 Fax



VIA FIRST CLASS MAIL

Commissioner for Patents and Trademarks Washington, D.C. 20231

Re:

Patent Application for:

"Circuit Structure Integrating the Power Distribution Functions of Circuits and Leadframes into the Chip Surface"

Serial No.:

09/975,630

Attorney Docket No.: TI-31678

Our File:

50000.2156

Dear Sir:

Enclosed for filing please find the following items relating to the above-identified application:

- (1) Election Document; and
- (2) Postcard.

Please file the above documents and return the date-stamped postcards to the corresponding addresses as indicated. In the meantime, if you have any questions or comments concerning this matter, please call the undersigned. Otherwise, please accept the enclosed.

Sincerely,

GCH:gle Enclosures